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COAT

The first exposure regions and the second exposure regions of the photoresist layer are then removed for forming an array photoresist layer on the thin film layer. The array photoresist layer functions as a mask to perform an etching process to the thin film layer for forming an array thin film layer as a storage nodes in the DRAM.

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In the claim:

1. (Once Amended) A method of forming storage nodes in a dynamic random access memory (DRAM) on a semiconductor wafer, the semiconductor wafer comprising a substrate, a thin film layer positioned on the substrate, and a photoresist layer positioned on the thin film layer, the method comprising:
 - 10 performing a first exposure process to form first exposure regions including a plurality of lines that are parallel to each other and covering each storage node;
 - 15 performing a second exposure process to form second exposure regions cutting the plurality of lines of the first exposure regions;
- 20 performing a development process on the first exposure regions and the second exposure regions of the photoresist layer;
- 25 removing the first exposure regions and the second exposure regions of the photoresist layer to form an array photoresist layer on the thin film layer; and

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Cancel claims 3-6.